

Notice of References Cited	Application/Control No. 10/530,899		Applicant(s)/Patent Under Reexamination DIEHL, ERIC	
	Examiner Ralph A. Verderamo III		Art Unit 2186	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,008,460	02-1977	Bryant et al.	711/136
*	B	US-5,513,336	04-1996	Vishlitzky et al.	711/136
	C	US-			
	D	US-			
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	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Jouppi et al., "Tradeoffs in Two-Level On-Chip Caching", 1994, IEEE Computer Society Press, pages 34 - 45
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.